

ABSTRACT OF THE DISCLOSURE

A method and apparatus for controlling address power of a plasma display panel and a plasma display panel device having that apparatus, wherein the plasma display panel includes pluralities of address electrodes, scan electrodes, and sustain electrodes, the scan electrodes and sustain electrodes forming pairs and being alternately disposed. After determining an attenuation coefficient that corresponds to a sum of pixel differences between lines throughout an input video signal, output video data is modified by multiplying the attenuation coefficient to the input video data. According to such a power control of address data, power consumption is reduced since the number of times of switching address data is reduced.

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